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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Sadra et al.

Docket No.: TI-35961

Serial No.: 10/694,237

Art Unit: 2813

Filed: 10/27/2003

Examiner: Schillinger, L.

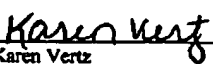
Title: Application of Different Isolation Schemes For Logic And Embedded Memory

AMENDMENT UNDER 37 CFR §1.111

May 10, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Commissioner:

CERTIFICATION OF FACSIMILE TRANSMISSION I hereby certify that the above correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below.	
 Karen Vertz	<u>5-10-05</u> Date

In response to the Office Action, dated 01/27/2005, in the above-identified patent application, please make the following amendments. They are respectfully submitted as a full and complete response to that Action. Charge any required fees to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.